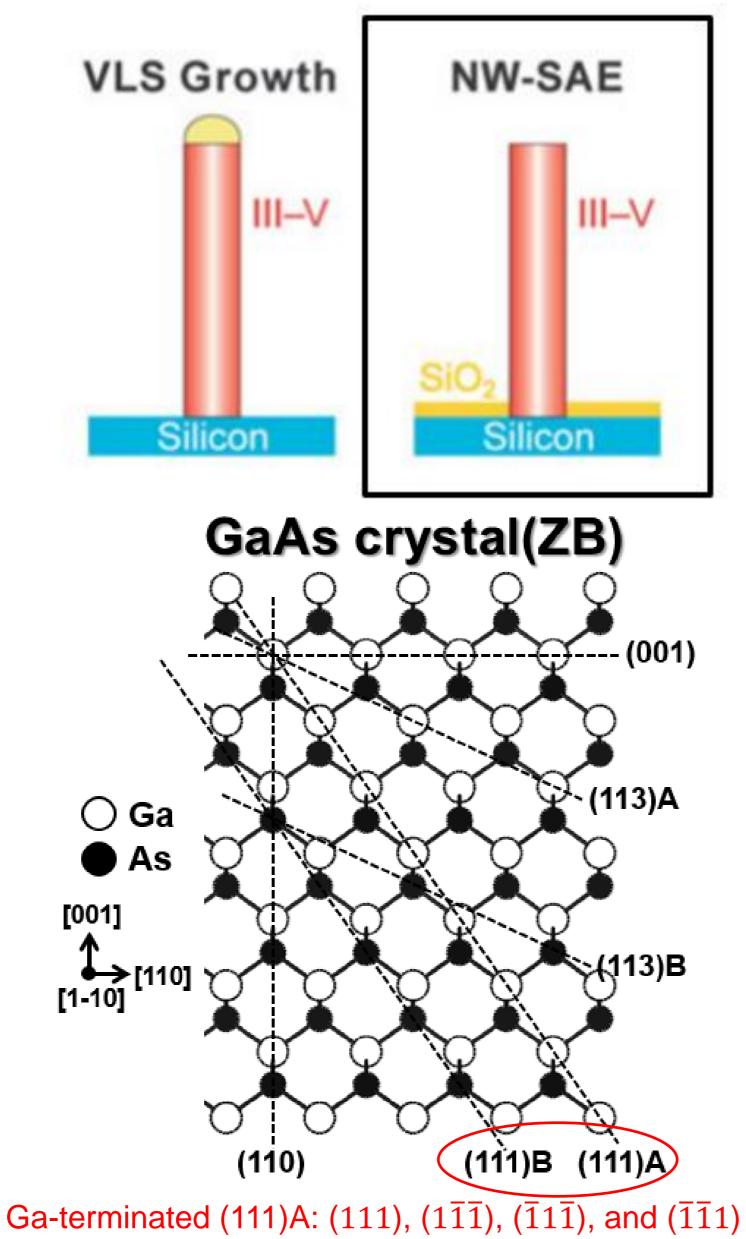
Theoretical understanding on the growth and stacking-fault mechanism of GaAs nanowires grown by catalyst-free method In Won Yeu^{1,2}, Gyuseung Han^{1,2}, Cheol Seong Hwang², and Jung-Hae Choi^{1*}

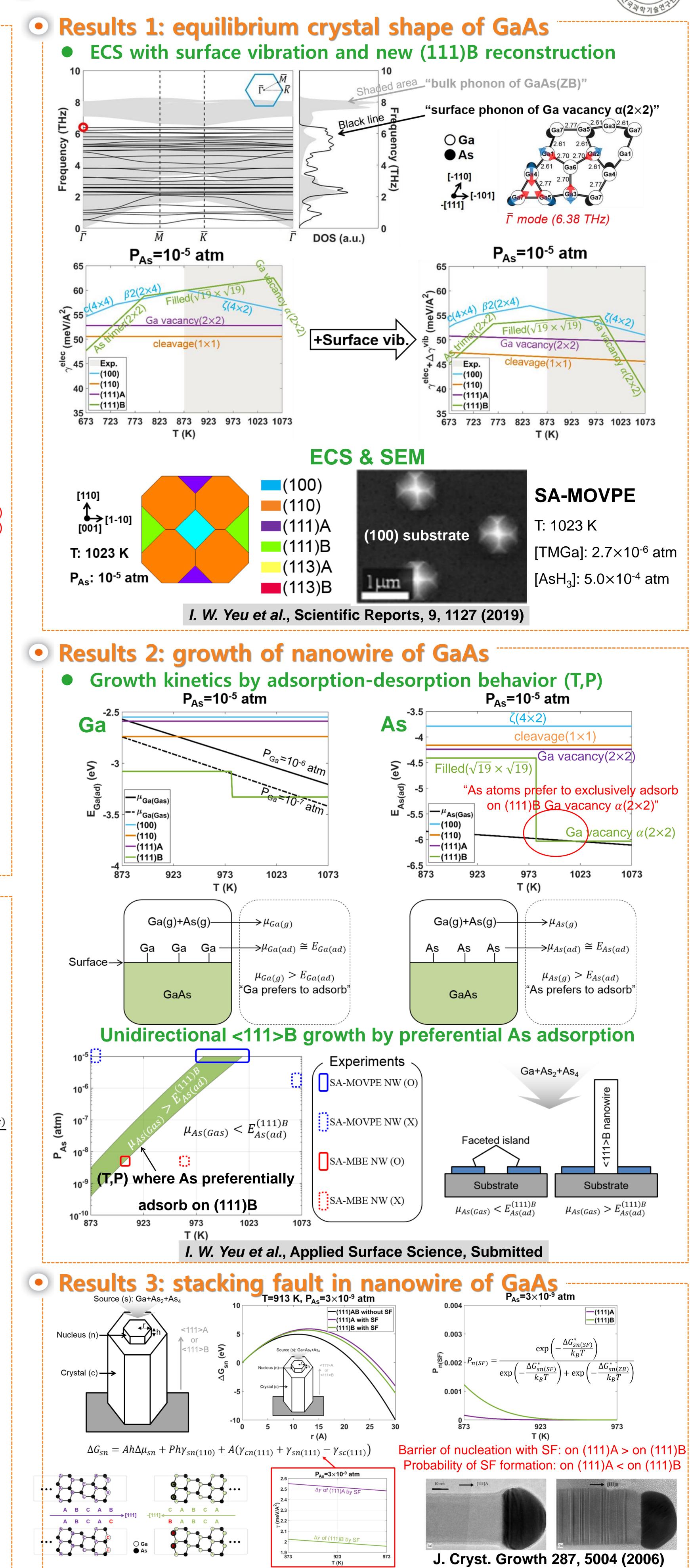
¹Center for Electronic Materials, Korea Institute of Science and Technology, Seoul 02792, Korea ²Department of Materials Science and Engineering, Seoul National University, Seoul 08826, Korea

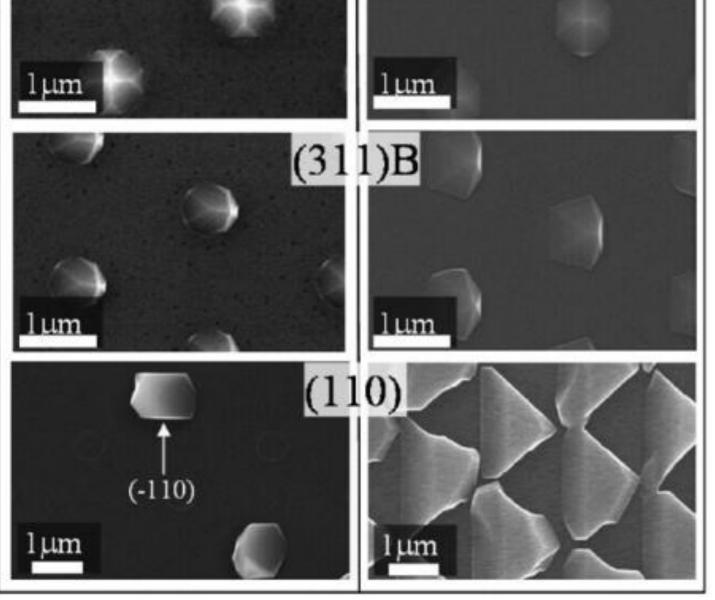
Computational Materials Design

 Introduction
 Introduction
 Integration of II-V NW on Si Temperature : 750°C [TMG] : 2.7 x10°6 atm [AsH₃] : 5.0x10°6 atm [AsH₃] : 5.0x10°4 atm
 Introduction
 Int

Anterials KIST





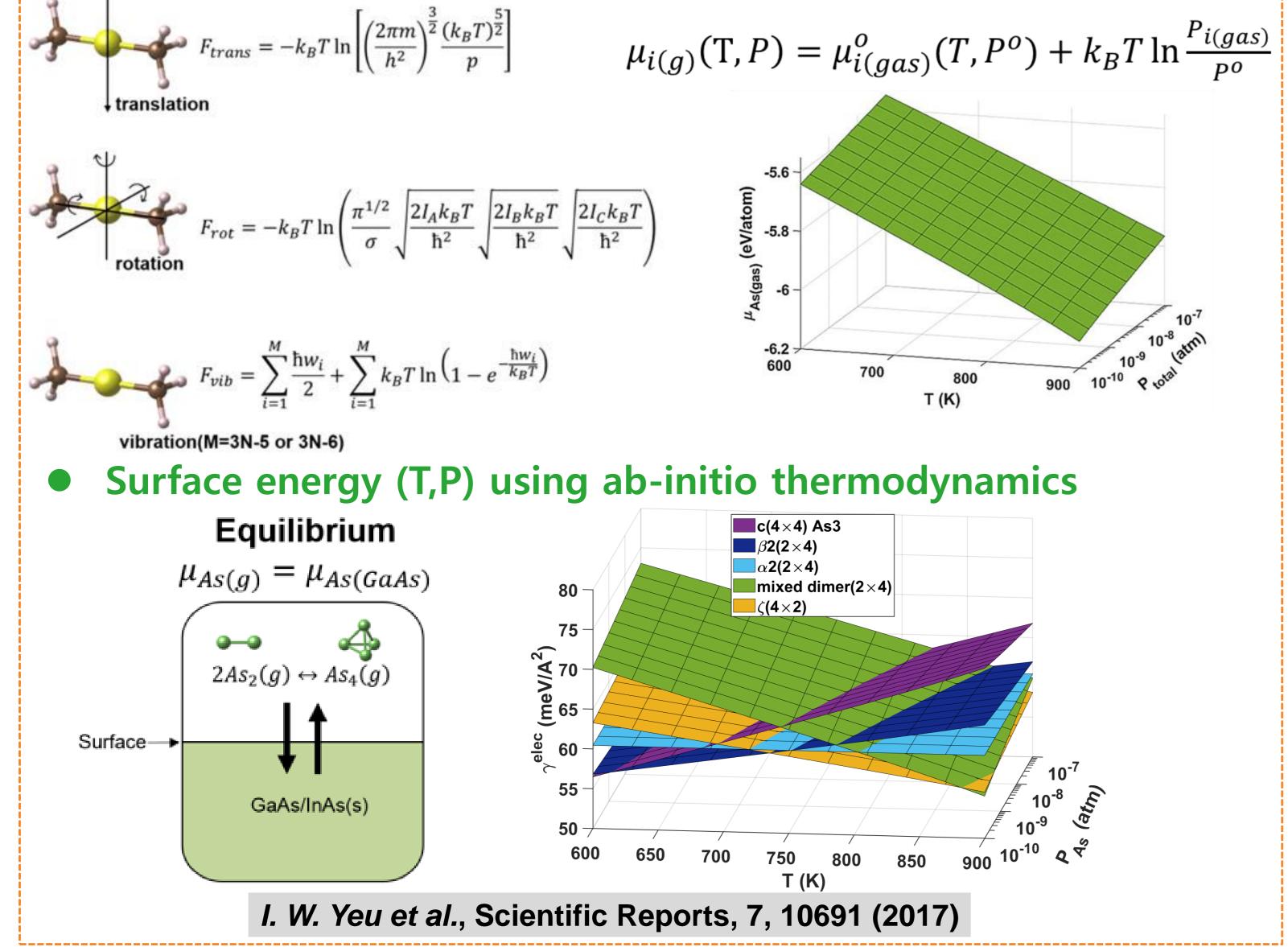


J. Cryst. Growth, 298, 616 (2007)

- Growth of GaAs depends on substrate orientation, growth
- temperature, and pressure. There are few theoretical studies about growth direction and
- condition dependence of the growth.
- Surface energies of various reconstructions were calculated as a function of (T, P) for several low-index orientations.
- The most stable reconstructions at a given growth condition were considered as adsorbent to the incoming adsorbates: Ga(g), As₂(g), As₄(g), whose effects on surface are described by chemical potential.
 Calculation results of thermodynamics (Equilibrium Crystal Shape, (ECS)) and kinetics (adsorption and nucleation) agree well with experiments and clearly unveil the growth mechanism of GaAs from the atomic process.

• Calculation Methods

- Density functional Theory (DFT) calculations
- Vienna ab initio Simulation Package(VASP)
- xc-functional: LDA
- Valence electrons: Ga[3d¹⁰4s²4p¹], In[4d¹⁰5s²5p¹], As[4s²4p³]
- Cutoff energy: 500 eV
- K-points: 12*12*12 for conventional zinc-blende cell
- Chemical potential (T,P) of gas phase using DFT



Contact: yiw0121@snu.ac.kr

제 15회 고등과학원 전자구조계산학회, KIAS, 7.4~5, 2019